

R&D Professionals

RACHIP SW & HW Professionals

About Us

Rachip is a conglomeration of Israel's top development service experts, working with the world's leading hi-tech companies to develop the next generation of hardware and software solutions.

Israel is the world's hi-tech laboratory, and Rachip is an integral part of it, partnering with global players at the cutting-edge of technological development.

The Rachip team is based on a carefully selected and meticulously trained group of women engineers from the Ultra-Orthodox (HAREDI) community, led by a group of highly experienced project managers. Optimal working conditions and promotion opportunities keep our workforce turnover levels extremely low, guaranteeing our clients a consistent level of professional service. This model offers the advantages of an offshore model coupled with the efficiency, loyalty and longevity of an in-house solution.

Rachip Cooperation Model

Rachip's structure and collective experience allows for absolute flexibility, with regards to project size, customer needs, and market requirements, while maintaining any possible model of implementation, such as:

On-Site • Off-Site • Monthly Retainer • Turn-Key Projects • Etc.

Advantages at a Glance

- Tailored solutions to suit specific client needs
- Competitive rates & flexibility on project scope & budget
- Fast response & short delivery times
- Highly experienced project managers
- Local, loyal, long-term professionals with little staff turnover
- Fortune 500 customers as well as startups



Rachip software development team is experienced in object-oriented design and programming, real-time applications and device drivers, as well as SW systems, mobile, and web applications.

Firmware, Embedded and Real Time Software Services

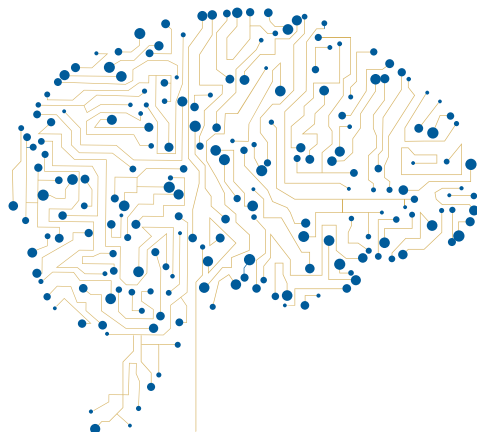
- Image processing
- Computer vision
- Machine learning
- Data and information fusion
- Object oriented design and programming
- Real time applications
- Device drivers and Kernel modules
- Embedded
- Multicore DSP SW Design (Ceva, TI, Freescale, etc)
- Firmware Verification
- ** C, C++, C#, .Net, Perl, python, Shell, Assembler

Web, Mobile and DB Software Services

- HTML5
- CSS3
- JavaScript, Ajax, jQuery
- PHP
- C#, ASP.NET
- iOS, Android Native Development
- JAVA
- Hybrid Applications (PhoneGap, Ionic, etc.)
- CMS Solutions (WordPress, Drupal, Joomla, etc.)
- UI / UX
- Databases (Oracle, SQL Server, MySQL, etc.)

Systems

- IOT
- Medical systems
- Unmanned and Autonomous System
- Ground vehicles
- Drones
- Robotics
- Control, navigation and data acquisition





The Rachip team has been involved in more than 100 tape outs in recent years, and has the capability to deliver a chip, from architecture and design to full manufacturing.

The Rachip teams are well versed in system architecture design, and can carry out RTL coding for a wide range of ASIC and FPGA chips. Services may include Design Analysis, Routing and Signal Integrity tests, and Synthesis & Static Timing analysis throughout the product's development lifecycle.

The Rachip verification team carries out SoC verification development and testing, utilizing advanced methodologies for a wide range of hardware development projects across many market sectors, such as Telecomm, Network Processing, Data Storage and various Consumer Electronics disciplines, while most processes are usually automated, through the additional development of complex and useful scripts.

ASIC/FPGA Design Services:

- System architecture
- RTL coding Verilog & VHDL
- Synthesis
- SystemC Modeling
- FPGA - Altera / Xilinx

SoC/Asic/FPGA Verification Services:

- SoC verification development and testing
- Coverage driven verification
- UVM / eRM / OVM / VMM environments
- SystemC Modeling
- Legacy language migration and maintenance

** e / Specman / System Verilog / SVA

Layout and Backend Services:

- LVS / PDK/ DRC libraries development
- Physical design and synthesis
- Static timing analysis
- Place and route
- Development of clocking and reset architectures
- Development of timing constraints
- Logic Synthesis
- Timing closure of Design
- Logic Equivalence Checking
- DFT

** Assura, Calibre, IC Compiler, Virtuoso.

Contact us

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Customers

